




**INFORMATION DISCLOSURE  
CITATION**

ATTY. DOCKET NO.

SERIAL NO.

550-235

09/876,220

APPLICANT

SWAINE

FILING DATE

GROUP

June 8, 2001

2183

(Use several sheets if necessary)

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
BB	5,802,273	9/1998	Levine et al.			
	4,590,550	5/1986	Eilert			
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	5,151,981	9/1992	Westcott et al.			
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**FOREIGN PATENT DOCUMENTS**

DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
BB 0 465 765	1/1992	EP			
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BB 0 919 919	6/1999	EP			

**OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)**

BB	ARM966E-S Technical Reference Manual, 12/1999, Arm Limited, Issue A, Chapter 1.
	Embedded Trace Macrocell Architecture Specification, 12/2002, Arm Limited, Issue 1, Chapters 1 and 2 and Appendix B.
	Uhlig et al., Trace-Driven Memory Simulation: A Survey, 6/1997, ACM Computing Surveys, Vol. 29, No. 2, pages 128-170.
	ARM IHI 0014 Revision C - Embedded Trace Macrocell Specification.
	ARM DDI 0157F - ETM9 (Rev. 1) Technical Reference Manual
	ARM DDI 0158D-ETM7 (Rev. 1) Technical Reference Manual.
↓	U.S. application serial number 09/773,387, filed February 1, 2001.
BB	ARM IHI 0014 Revision I-Embedded Trace Macrocell Spec.

\*Examiner

Brett Bucher

Date Considered

2/9/05

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

**OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)**

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